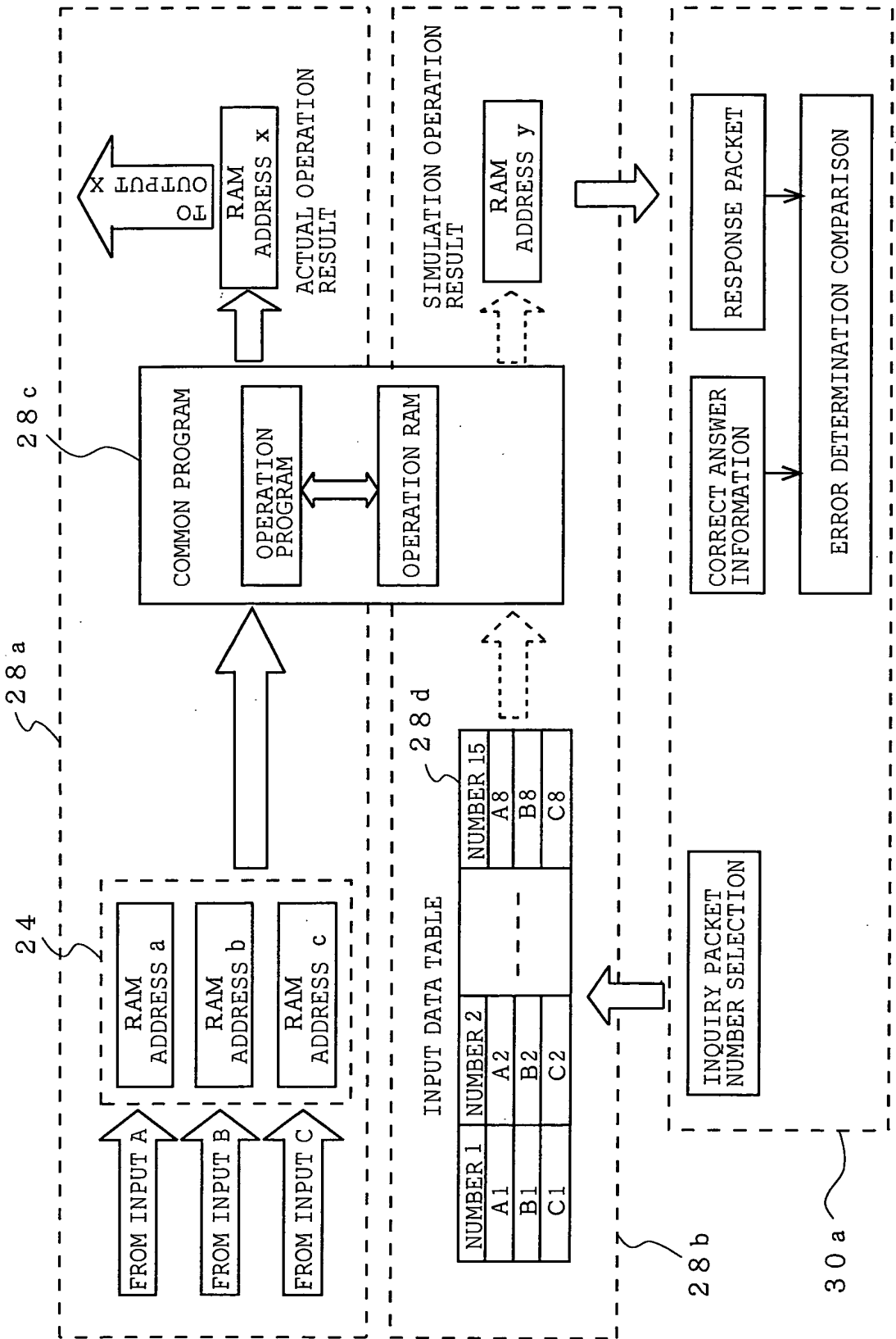


10a : ELECTRONIC CONTROL UNIT  
25a : NON-VOLATILE PROGRAM MEMORY  
29, 49 : SERIAL INTERFACE CIRCUIT  
30a : MONITORING CONTROL CIRCUIT SECTION  
40 : DATA PROCESSING CIRCUIT  
WD : WATCHDOG SIGNAL

FIG. 1



28a : UNDER-TEST TARGET PROGRAM  
28b : SIMULATION-OPERATION-EXECUTION PROCESSING PROGRAM  
30a : MONITORING CONTROL CIRCUIT SECTION

FIG. 2

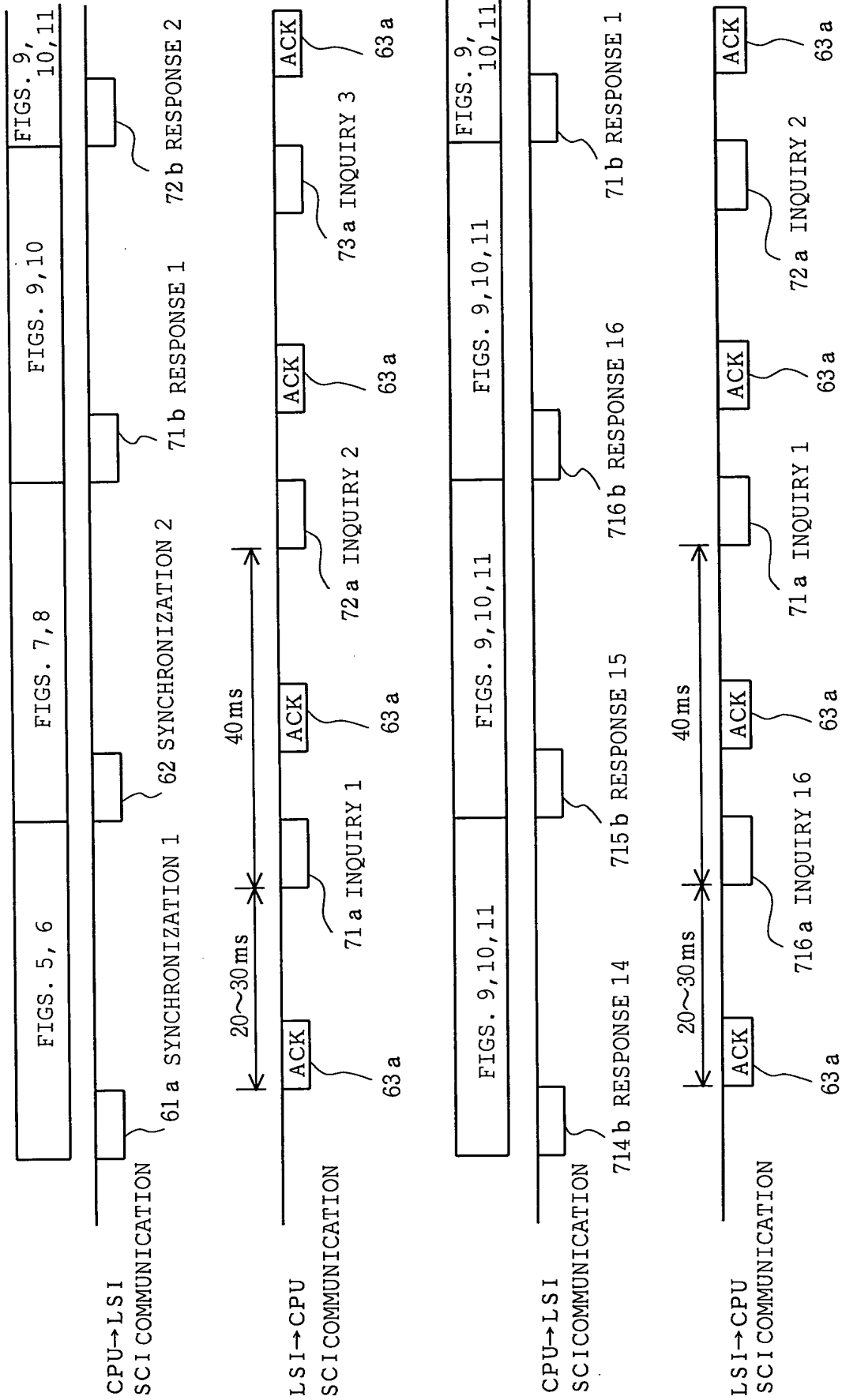


FIG. 3

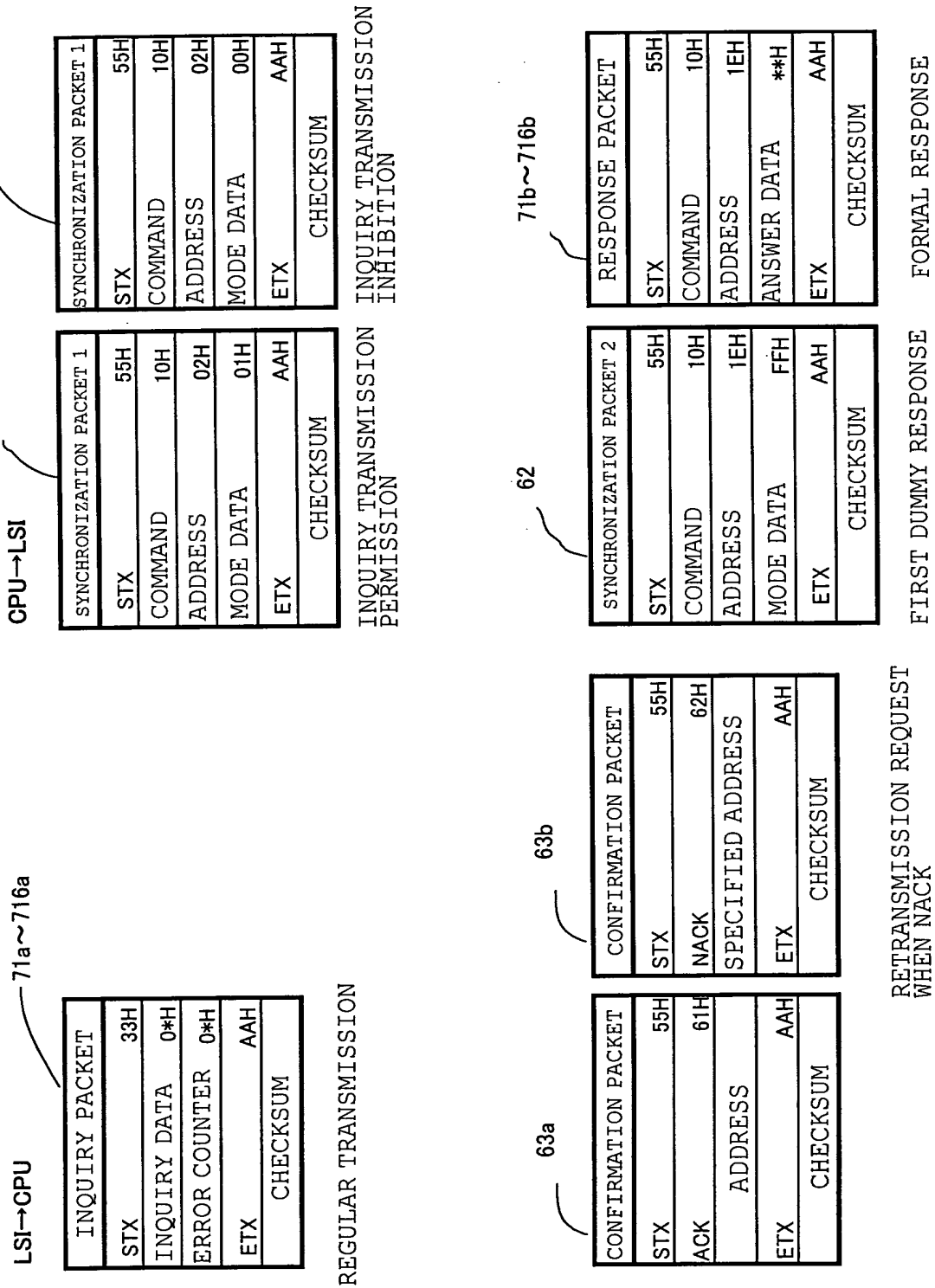


FIG. 4

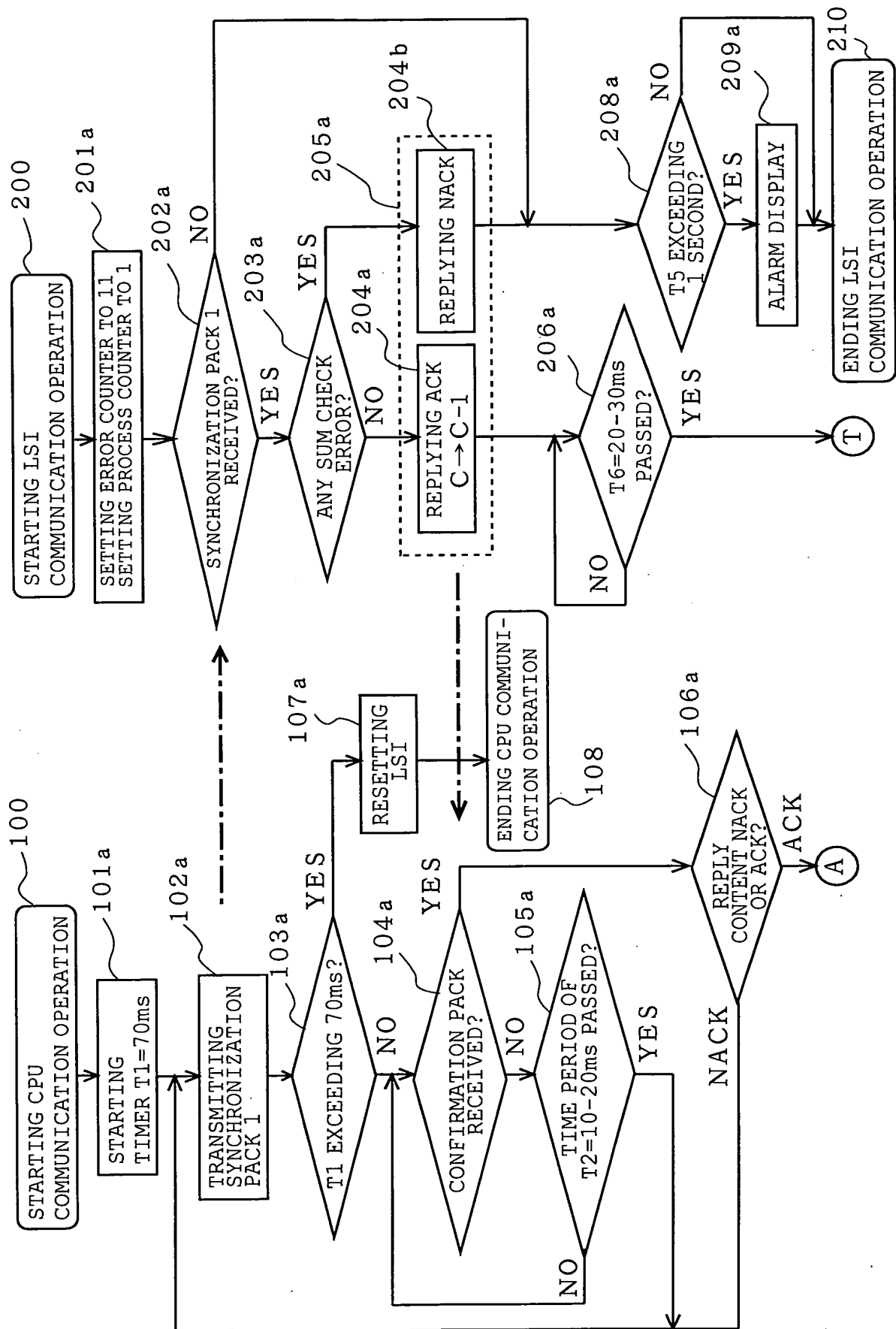


FIG. 5

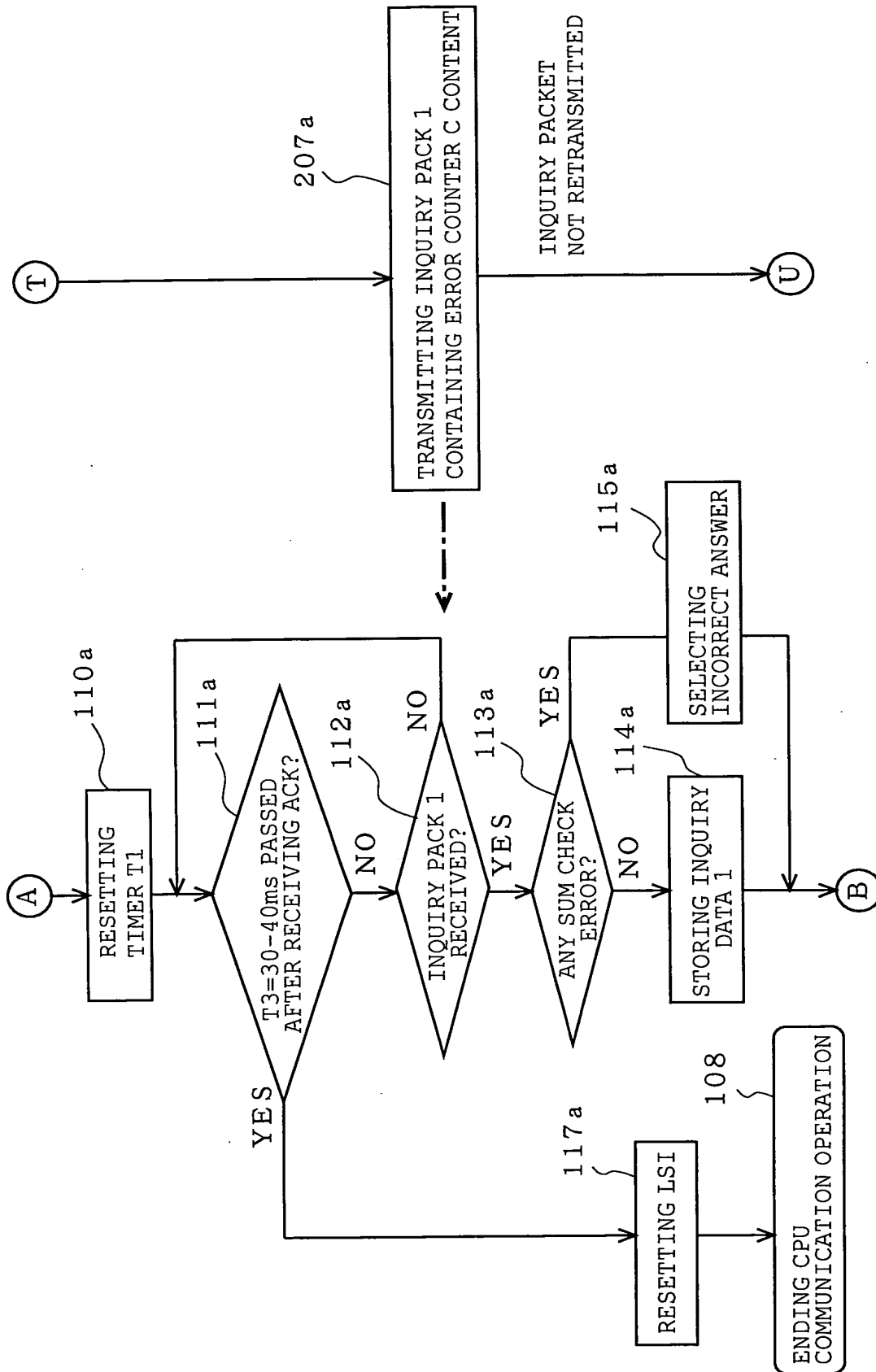


FIG. 6

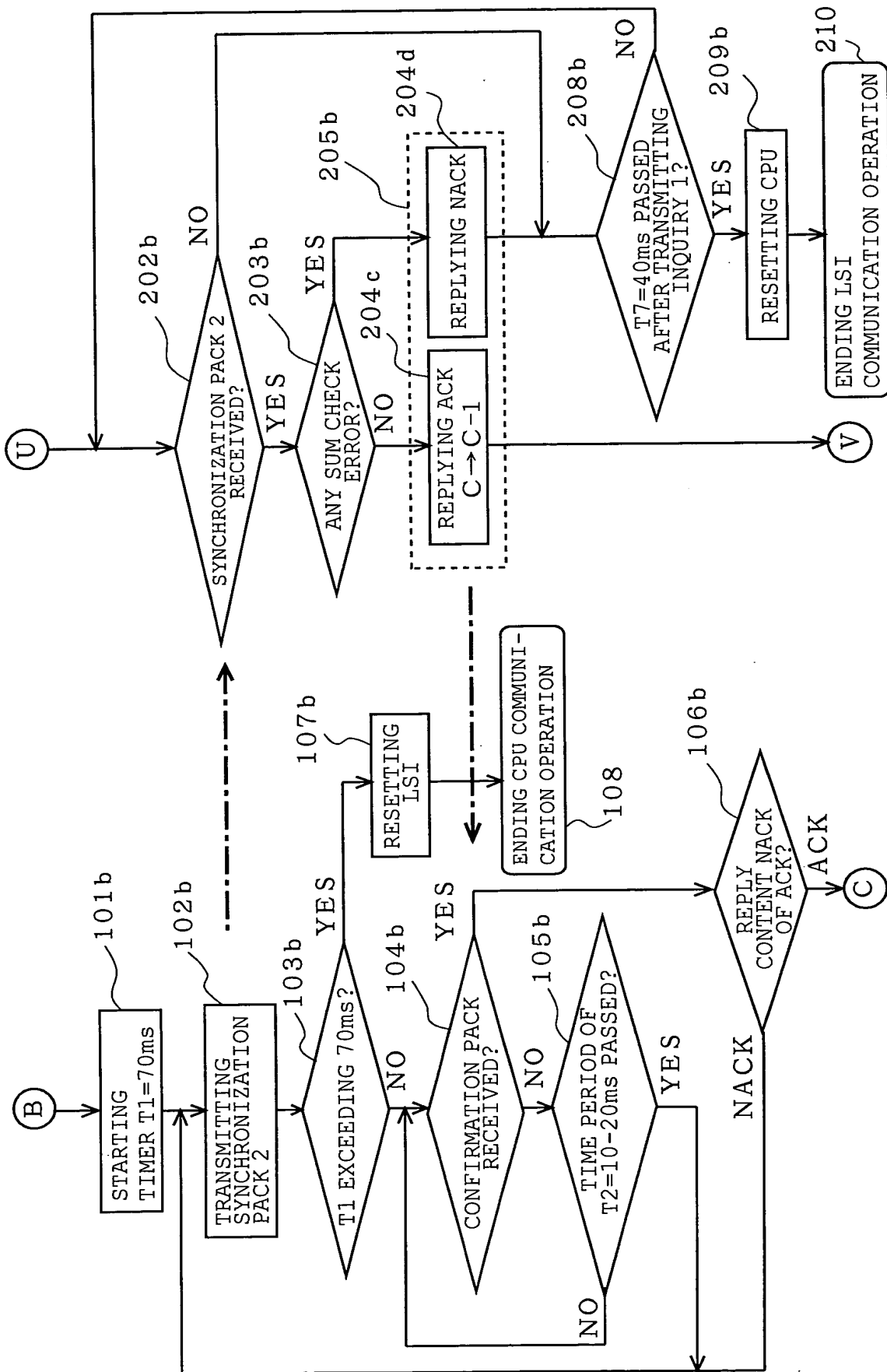


FIG. 7

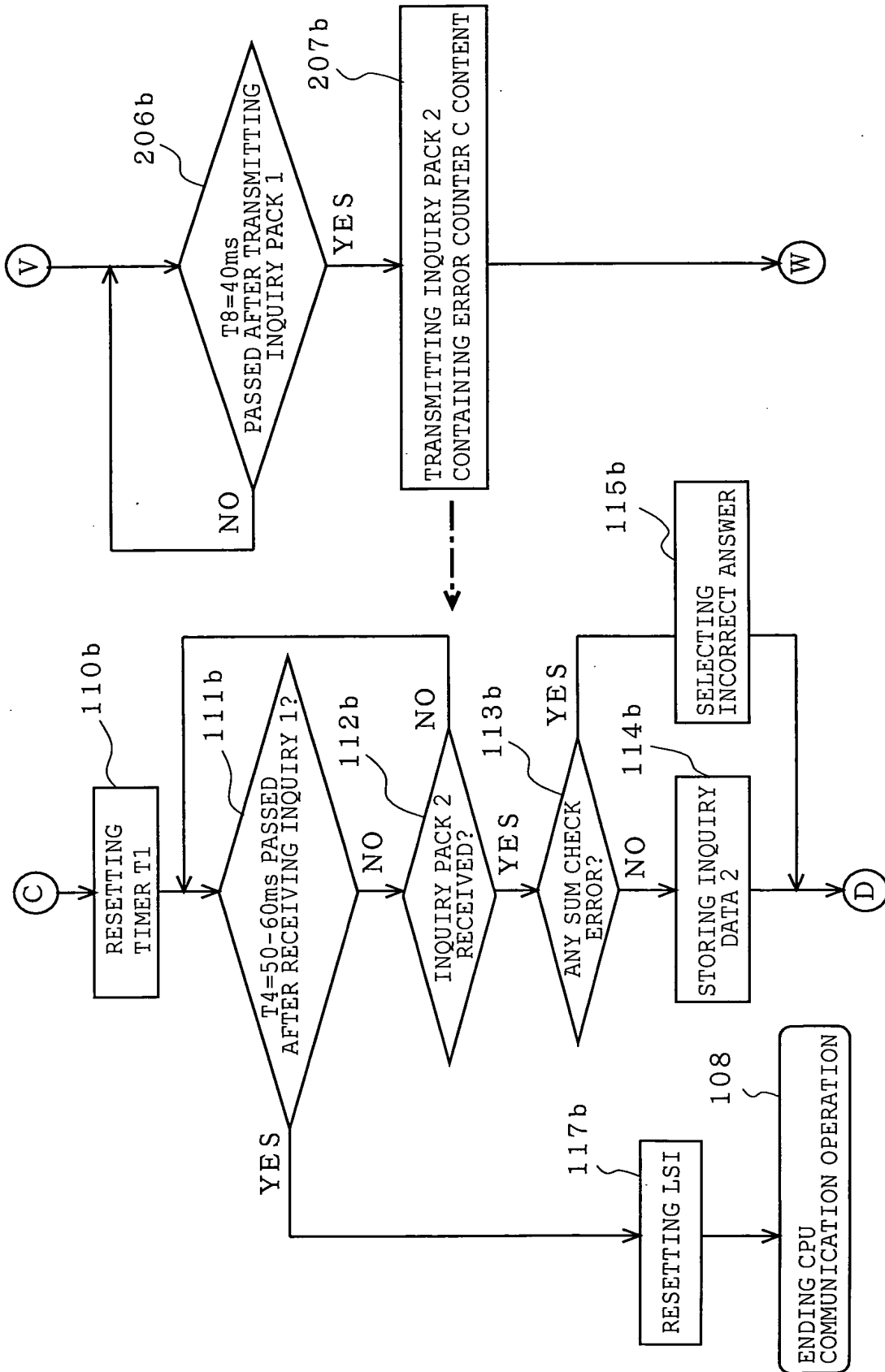


FIG. 8



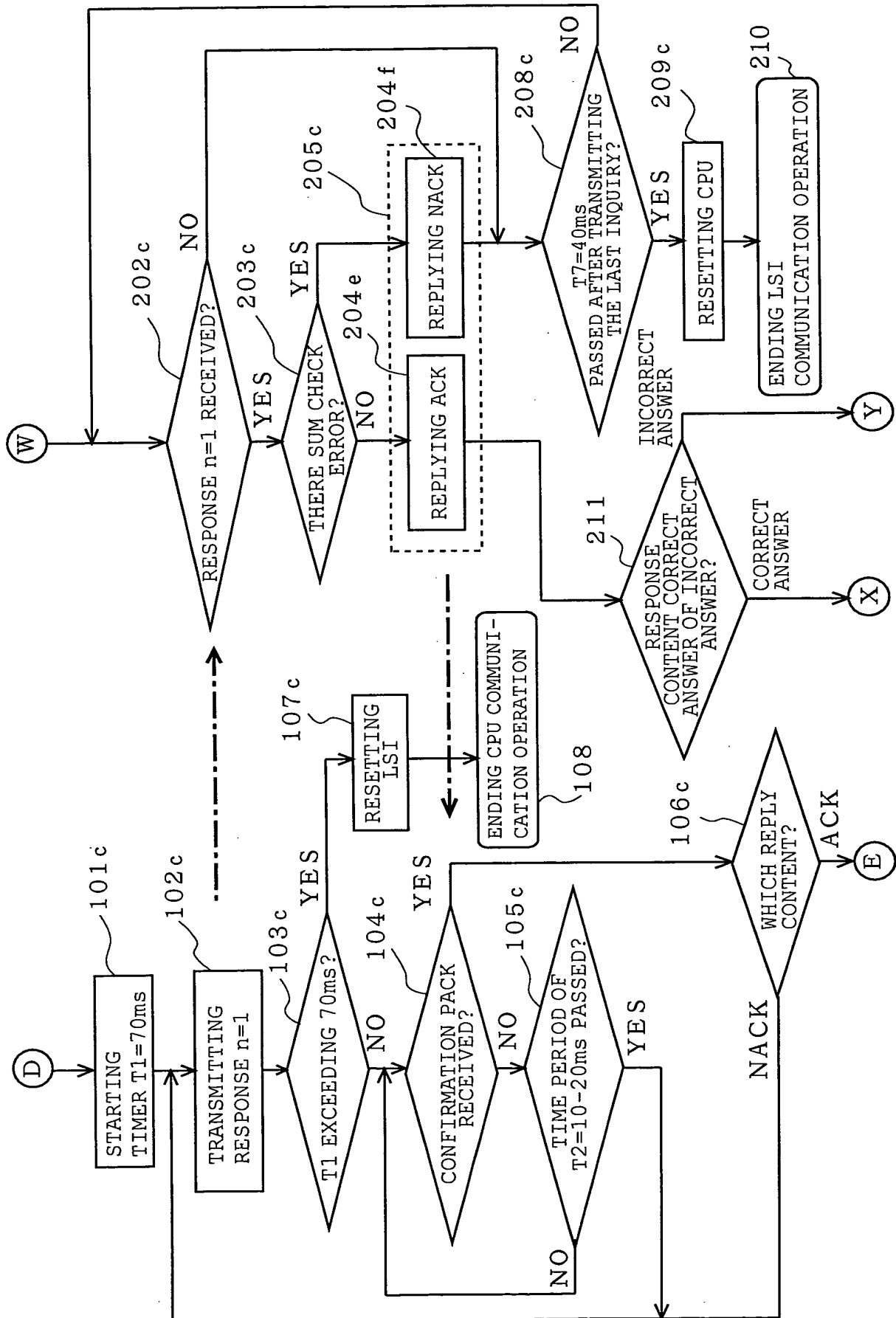


FIG. 9

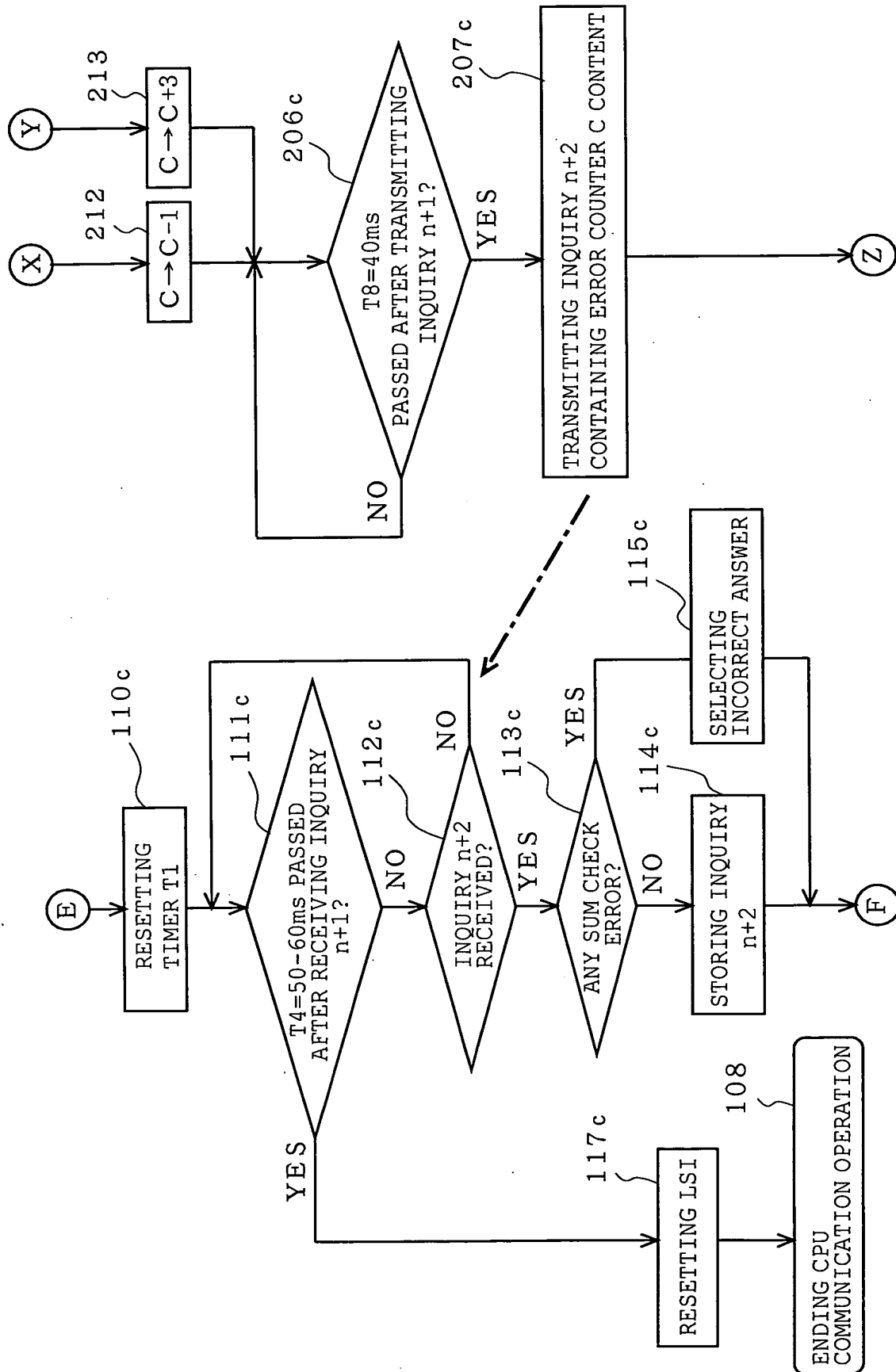


FIG. 10

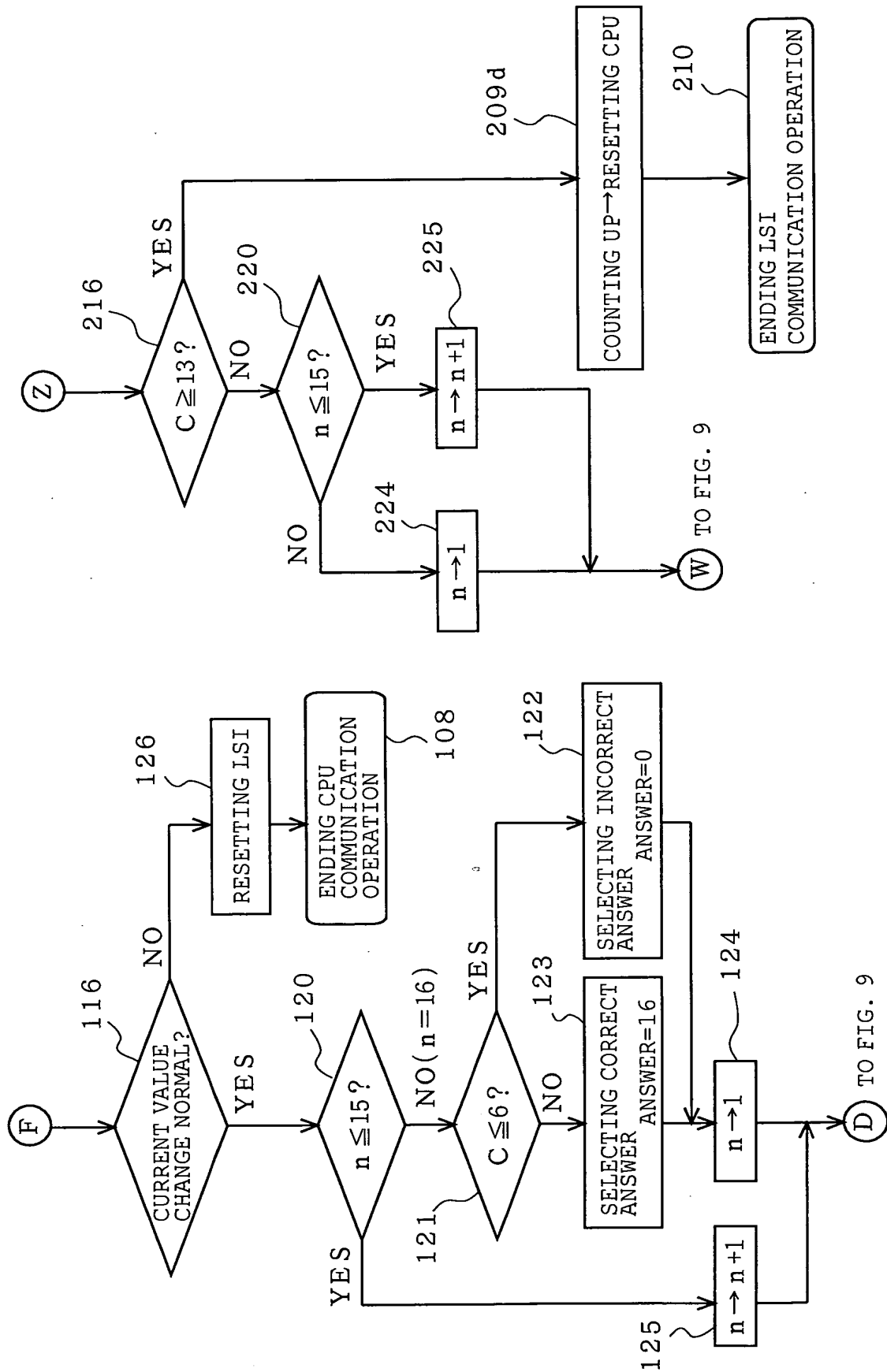
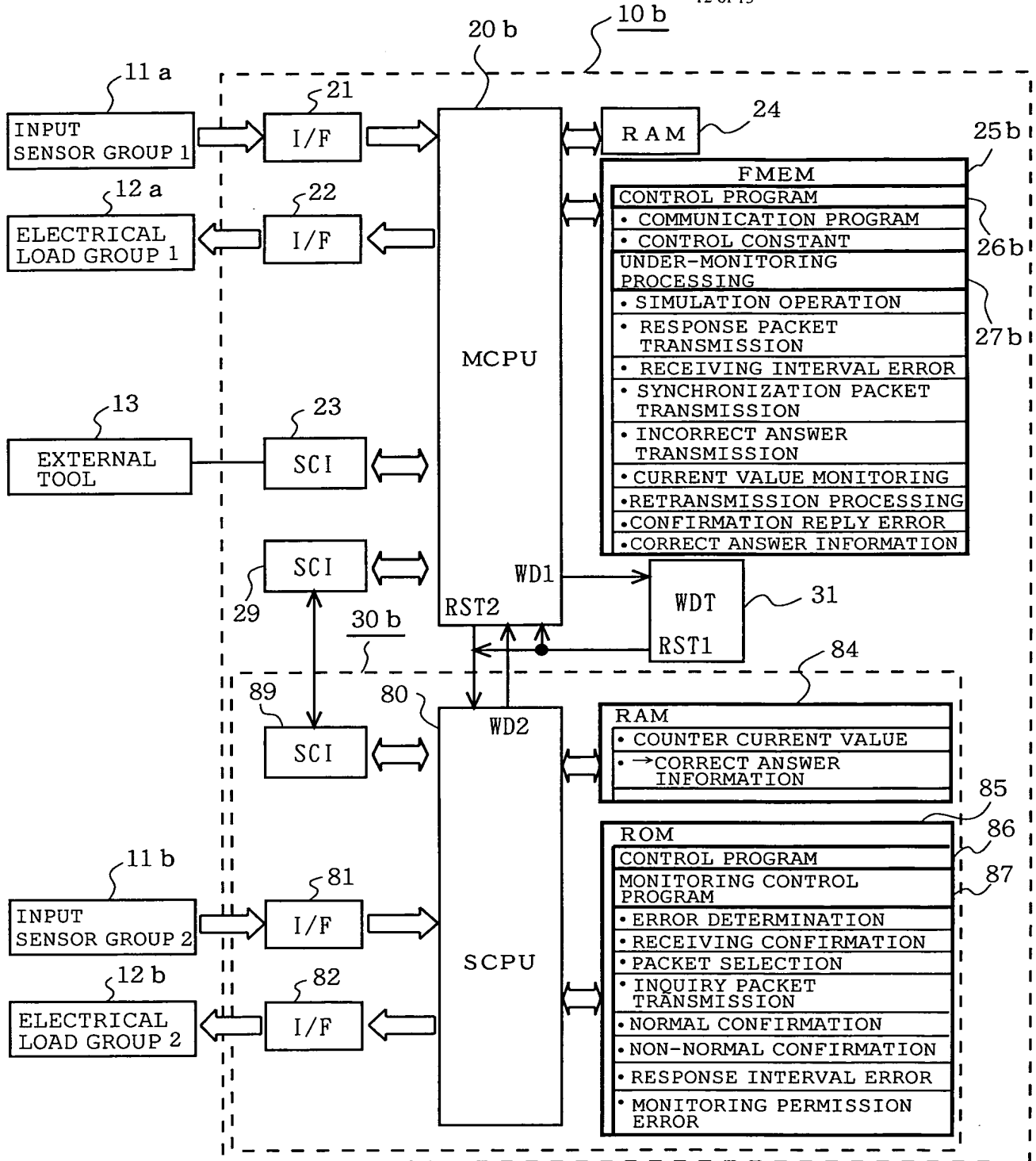


FIG. 11



10b : ELECTRONIC CONTROL UNIT  
25b : NON-VOLATILE PROGRAM MEMORY  
29, 49 : SERIAL INTERFACE CIRCUIT  
30b : MONITORING CONTROL CIRCUIT SECTION  
31 : WATCHDOG TIMER  
80 : AUXILIARY MICROPROCESSOR  
84 : AUXILIARY RAM MEMORY  
85 : AUXILIARY PROGRAM MEMORY

FIG. 12

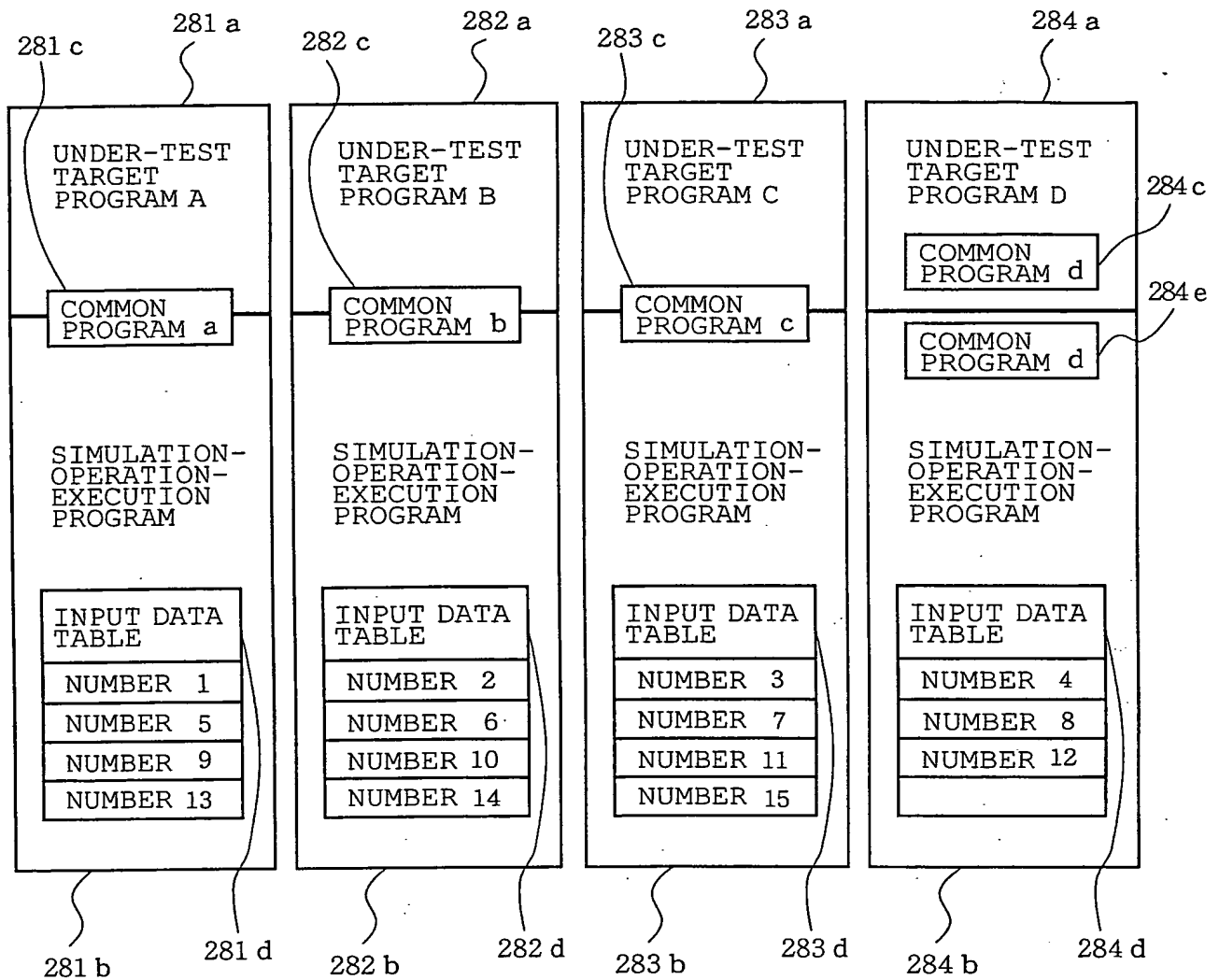
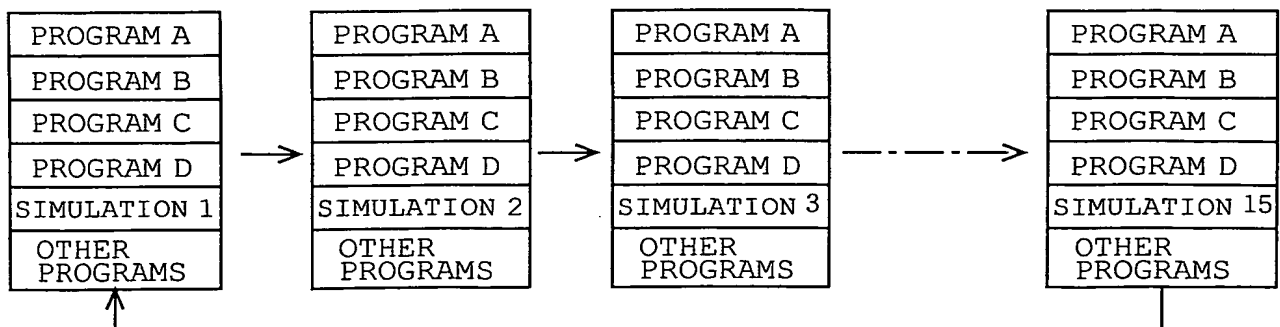


FIG. 13(A)



281b-284b : SIMULATION-OPERATION-EXECUTION PROCESSING PROGRAM

FIG. 13(B)